

UTILITY PATENT APPLICATION TRANSMITTAL

Attorney Docket No. 241190US2

COPY

First Inventor or Application Identifier Takeshi YOSHIDA

Title SEMICONDUCTOR INTEGRATED CIRCUIT HAVING SYSTEM BUS DIVIDED IN STAGES

Assignee Name: KABUSHIKI KAISHA TOSHIBA

Assignee Address: 1-1, Shibaura 1-chome, Minato-ku, Tokyo, Japan

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents

ADDRESS TO: Commissioner for Patents
Mail Stop Patent Application
Alexandria, Virginia 22313

1. ☒ Fee Transmittal Form (e.g. PTO/SB/17)
(Submit an original and a duplicate for fee processing)
2. ☒ Specification Total Sheets
3. ☒ Drawing(s) (35 U.S.C. 113) Total Sheets
4. ☐ Oath or Declaration Total Pages
- a. ☐ Newly executed (original or copy)
- b. ☐ Copy from a prior application (37 C.F.R. §1.63(d))
(for continuation/divisional with box 17 completed)
 - i. ☐ DELETION OF INVENTOR(S)
Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).
5. ☐ CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)
6. ☐ Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)
 - a. ☐ Computer Readable Form (CRF)
 - b. Specification or Sequence Listing on :
 - i. ☐ CD-ROM or CD-R (2 copies); or
 - ii. ☐ Paper
 - c. ☐ Statements verifying identity of above copies

- ### ACCOMPANYING APPLICATION PARTS
7. ☐ Assignment Papers (cover sheet & document(s))
 8. ☒ Application Data Sheet. See 37 CFR 1.76
 9. ☐ 37 C.F.R. §3.73(b) Statement (when there is an assignee) ☐ Power of Attorney
 10. ☐ English Translation Document (if applicable)
 11. ☒ Information Disclosure Statement (IDS)/PTO-1449 ☒ Copies of IDS Citations (1)
 12. ☐ Preliminary Amendment
 13. ☒ White Advance Serial No. Postcard
 14. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)
 15. ☐ Applicant claims small entity status. See 37 CFR 1.27
 16. ☒ Other: Statement of Relevancy

17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application no.:

Prior application information: Examiner: Group Art Unit:

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

18. Amend the specification by inserting before the first line the sentence:

☐ This application is a ☐ Continuation ☐ Division ☐ Continuation-in-part (CIP)

of application Serial No.

Filed on

☐ This application claims priority of provisional application Serial No.

Filed

19. CORRESPONDENCE ADDRESS



22850

(703) 413-3000

FACSIMILE: (703) 413-2220

Name:	Marvin I. Spivak	Registration No.:	24,913
Signature:		Date:	2/23/04
Name:		Registration No.:	

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MJE

OSMM&N File No. 241190US2

Dept.: PP/JF

By: MJS/als

Serial No. New Application

In the matter of the Application of: Takeshi YOSHIDA

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING SYSTEM BUS
DIVIDED IN STAGES

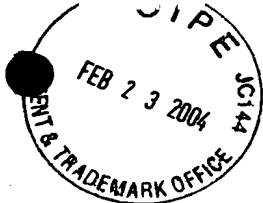
Due Date: 4/25/04

The following has been received in the U.S. Patent Office on the date stamped hereon:

- 28 pp. Specification 11 Claims/Drawings 5 Sheets and
2 Pages Application Data Sheet
- Utility Patent Application Transmittal
- Check for \$880.00
- Dep. Acct. Order Form
- Fee Transmittal Form
- Information Disclosure Statement
- PTO-1449
- Cited References 1
- Statement of Relevancy
- White Advance Serial Number Card



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**COPY**

Docket No. 241190US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Takeshi YOSHIDA

SERIAL NO: New Application

FILING DATE: Herewith

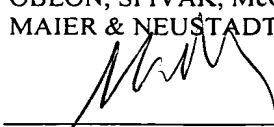
FOR: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING SYSTEM BUS DIVIDED IN STAGES

FEE TRANSMITTALCOMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	11 - 20 =	0	x \$18 =	\$0.00
INDEPENDENT CLAIMS	1 - 3 =	0	x \$84 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$280 =	\$0.00
<input checked="" type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$130.00
BASIC FEE				\$750.00
TOTAL OF ABOVE CALCULATIONS				\$880.00
<input type="checkbox"/> REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
<input type="checkbox"/> FILING IN NON-ENGLISH LANGUAGE			+ \$130 =	\$0.00
<input type="checkbox"/> RECORDATION OF ASSIGNMENT			+ \$40 =	\$0.00
TOTAL				\$880.00

- ☐ Please charge Deposit Account No. 15-0030 in the amount of A duplicate copy of this sheet is enclosed.
- ☒ A check in the amount of \$880.00 to cover the filing fee is enclosed.
- ☒ The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.
Marvin J. Spivak
Registration No. 24,913Robert T. Pous
Registration No. 29,099**22850**Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 05/03)**BEST AVAILABLE COPY**



Docket No.: 241190US2

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313



RE: Application Serial No.: 10/632,866
Applicants: Takeshi YOSHIDA
Filing Date: August 4, 2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT
HAVING SYSTEM BUS DIVIDED IN STAGES
Group Art Unit: N/A
Examiner: DILLARD, Delora

SIR:

Attached hereto for filing are the following papers:

ATTORNEY DECLARATION

COPY OF DATE-STAMPED FILING RECEIPT DATED AUGUST 4, 2003

COPY OF APPLICATION AS FILED ON AUGUST 4, 2003

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Customer Number

22850

(703) 413-3000 (phone)
(703) 413-2220 (fax)

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Marvin J. Spivak

Registration No. 24,913

Robert T. Pous
Registration No. 29,099